

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)
Tomoharu TANAKA et al.) Group Art Unit: 2824 (parent)
)
Serial No.: Rule 53(b) Continuation Application of) Examiner: Son T. Dinh (parent)
Serial No. 10/315,030)
) Attorney Docket No. 001701.00193
Filed: herewith)
)
For: SEMICONDUCTOR MEMORY DEVICE)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with Applicants' duty of disclosure, the following information is submitted for consideration by the United States Patent and Trademark Office in connection with the above-captioned application. The information is identified on the attached PTO/SB/08A form.

This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior U.S. patent application Serial No. 10/315,030 (filed December 10, 2002), which relied in turn on the earlier filing date of prior U.S. patent application Serial No. 10/024,189 (filed December 21, 2001), which relies in turn on the earlier filing date of prior U.S. patent application Serial No. 09/768,588 (filed January 25, 2001), which relies in turn on the earlier filing date of prior U.S. patent application Serial No. 09/504,903 (filed February 16, 2000, now U.S. Pat. No. 6,208,573), and which relies in turn on the earlier filing date of prior U.S. patent application Serial No. 09/055,216 (filed April 6, 1998, now U.S. Pat. No. 6,064,611). The references identified on the attached PTO/SB/08A form were submitted to and/or cited by the USPTO in one or more of these prior applications. Pursuant to 37 C.F.R. §1.98(d), copies of these references are not

Continuation Application of
Serial No. 10/315,030

Attorney Docket No.: 01701.00193

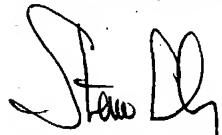
included with this paper. Applicants would be happy to supply additional copies of these references upon request.

Applicants do not waive any rights to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application. Applicants also wish to bring to the attention of the Examiner commonly assigned, Patent Nos. 6,028,792 and 5,847,992, which are related to this application by subject matter.

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed PTO/SB/08A form in the appropriate place to indicate that the document has been considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

The present paper is being filed concurrently with the present application, and Applicants believe that no fee is necessary pursuant to 37 C.F.R. § 1.97(b)(1). However, if a fee is due, the Commissioner is authorized to charge Deposit Account No. 19-0733.

Respectfully submitted,

 #42,402

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Registration No. 28,175

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Dated: July 21, 2003

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Substitute for form 1449A/PTO		<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	TBA; Continuation of SN 10/315,030
(use as many sheets as necessary)		Filing Date	herewith
		First Named Inventor	Tomoharu Tanaka
		Group Art Unit	2824 (parent)
		Examiner Name	Son T. Dinh (parent)
Sheet	1	of	2
		Attorney Docket Number	001701.00193

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		JP 08-279297	10-22-1996	Samsung Electron Co Ltd.		Abstr.
		JP 07-230695	08-29-1995	Toshiba Corp.		Abstr.

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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Substitute for form 1449A/PTO			<i>Compl. te if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Application Number	TBA; Continuation of SN 10/315,030
Sheet	2		Filing Date	herewith
			First Named Inventor	Tomoharu Tanaka
			Group Art Unit	2824 (parent)
			Examiner Name	Son T. Dinh (parent)
			Attorney Docket Number	001701.00193

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Tae-Sung JUNG et al.; "A3,3V 128 Mb Multi-Level NAND Flash Memory for Mass Storage Applications," ISSCC Digest of Technical papers, pp. 32-33, Feb. 1996	
		Tae-Sung JUNG et al.; "A 117-mm ² 3.3-V 128-Mb Multilevel NAND Flash Memory For Mass Storage Applications," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1575-1583	
		Young, Joon Choi et al.; "A High Speed Programming Scheme For Multi-Level NAND Flash Memory," Samsung Electronics Co., Ltd., Kiheung, Korea, 1996 Symposium on VLSI Circuits Digest of Technical Papers, pp. 170-171	

Examiner Signature		Date Considered
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¹ Unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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